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Total Number of Pages in This Submission

Application Number 10/814,866

Filing Date | March 30, 2004

First Named Inventor Christopher J. Diorio

Art Unit 2818

Examiner Name **Unassigned**

Attorney Docket Number IMPJ-0027B

ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input type="checkbox"/> Other Enclosure(s) (please identify below):
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm	THELEN REID & PRIEST LLP		
Signature			
Printed Name	James M. Wu		
Date	1/19/06	Reg. No.	45,241

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Signature	Beatrice Orozco	Date	1/19/2009
Typed or printed name	Beatrice Orozco		

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



APPLICANT: Christopher J. Diorio et al.

SERIAL NO.: 10/814,866

FILING DATE: March 30, 2004

TITLE: Rewriteable Electronic Fuses

EXAMINER: Unassigned

ART UNIT: 2818

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: 1/19/08

Name: Beatrice Orozco
Beatrice Orozco

**MAIL STOP: AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
ALEXANDRIA, VA 22313-1450**

INFORMATION DISCLOSURE STATEMENT

Each item of information listed in the attached FORM PTO-1449, for which a copy of each is attached (unless the blanket waiver referred to below applies), may be material to the examination of the above-identified application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR §§ 1.56, 1.97 and 1.98. The Examiner is requested to review, consider and document each such item in the official record of this application.

Note: If this box is checked, this case was filed after June 30, 2003 and qualifies for the blanket waiver of deposit of copies of U.S. Patents and U.S. Patent Application Publications in

accordance with the written waiver of 37 CFR §1.98 (a)(2)(i) dated July 11, 2003. Accordingly, such copies are not attached.

This Information Disclosure Statement under 37 CFR §§ 1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that any one or more of these items constitutes prior art.

I

This statement is filed pursuant to (**CHECK ONE BOX**):

37 C.F.R. § 1.97(b).
This information disclosure statement is filed either:
(1) within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);
(2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application;
(3) before the mailing date of a first office action on the merits; or
(4) before the mailing of a first office action after the filing of a Request for Continued Examination under 37 C.F.R. §1.114, **whichever event occurs last.**

Accordingly, this information disclosure statement requires no fee and no certification.

37 C.F.R. § 1.97(c).
This information disclosure statement is filed **after** the period specified in 37 C.F.R. § 1.97(b), but **before** the mailing date of any of the following:
(1) a final action under 37 C.F.R. § 1.113;
(2) a notice of allowance under 37 C.F.R. § 1.311; or
(3) an action that otherwise closes prosecution in the application.

Accordingly, this information disclosure statement requires either:

(1) the fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c); or
(2) a certification according to 37 C.F.R. § 1.97 (e)(1) or (2).

37 C.F.R. § 1.97(d).
This information disclosure statement is filed **after** the period specified in 37 C.F.R. § 1.97 (c).

Accordingly, this information disclosure statement requires:

- (1) a certification in accordance with 37 C.F.R. § 1.97(e); and
- (2) the fee specified in 37 C.F.R. § 1.17 (p) to consider an information disclosure statement under 37 C.F.R. § 1.97(d).

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. § 1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. **To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.**

II

Fees Due (CHECK ONE BOX):

No fee is due.

The fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c) or 37 C.F.R. § 1.97(d) is enclosed (\$180).

III

Certification (CHECK ONE BOX):

No certification is necessary.

Pursuant to 37 C.F.R. § 1.97(e)(1), the undersigned hereby certifies: That each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

Pursuant to 37 C.F.R. § 1.97(e)(2), the undersigned hereby certifies: No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this information disclosure statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this information disclosure statement.

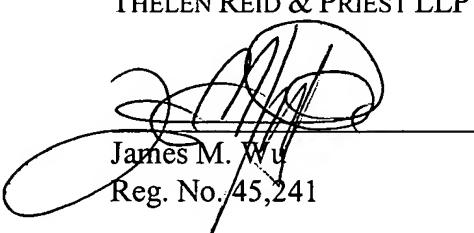
IV

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-1698.

Respectfully submitted,
THELEN REID & PRIEST LLP

Dated: January 19, 2006


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Form PTO 1449 (Rev. 2-32)		Department of Commerce Patent and Trademark Office	Atty. Docket No. IMPJ-0027B	Serial No. 10/814,866
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Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant: Christopher J. Diorio et al. Filed: March 30, 2004		Group: 2818
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U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date
	A	4,158,239	1/12/1979	Bertin			
	B	4,541,073	9/10/1985	Brice et al.			
	C	4,758,869	7/19/1988	Eitan et al.			
	D	4,935,702	6/19/1990	Mead et al.			
	E	5,018,102	5/21/1991	Houston			
	F	5,068,622	11/26/1991	Mead et al.			
	G	5,438,542	8/1/1995	Atsumi et al.			
	H	6,141,247	10/31/2000	Roohparvar et al.			
	I	6,214,666	4/10/2001	Mehta			
	J	6,363,006	3/26/2002	Naffziger et al.			
	K	6,363,011	3/26/2002	Hirose et al.			
	L	6,456,992	9/24/2002	Shibata et al.			
	M	6,469,930	10/22/2002	Murray			
	N	6,573,765	6/3/2003	Bales et al.			
	O	6,633,188	10/14/2003	Jia et al.			
	P	6,845,029	1/18/2005	Santin et al.			
	Q	6,946,892	9/20/2005	Mitarashi			

Foreign Documents

Translation

Init.		Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

R	Declercq et al., "Design and Optimization of High-Voltage CMOS Devices Compatible With a Standard 5 V CMOS Technology", IEEE Custom Integrated Circuits Conference, 1993, pp. 24.6.1-24.6.4.
S	Dickson, "On-Chip High-Voltage Generation in MNOS Integrated Circuits Using an Improved Voltage Multiplier Technique", IEEE Journal of Solid-State Circuits, Vol. SC-11, No. 3, June 1976, pp. 374-378.
T	International Search Report for PCT/US 03/31792 date mailed August 12, 2004.

Examiner	Date Considered
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Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.